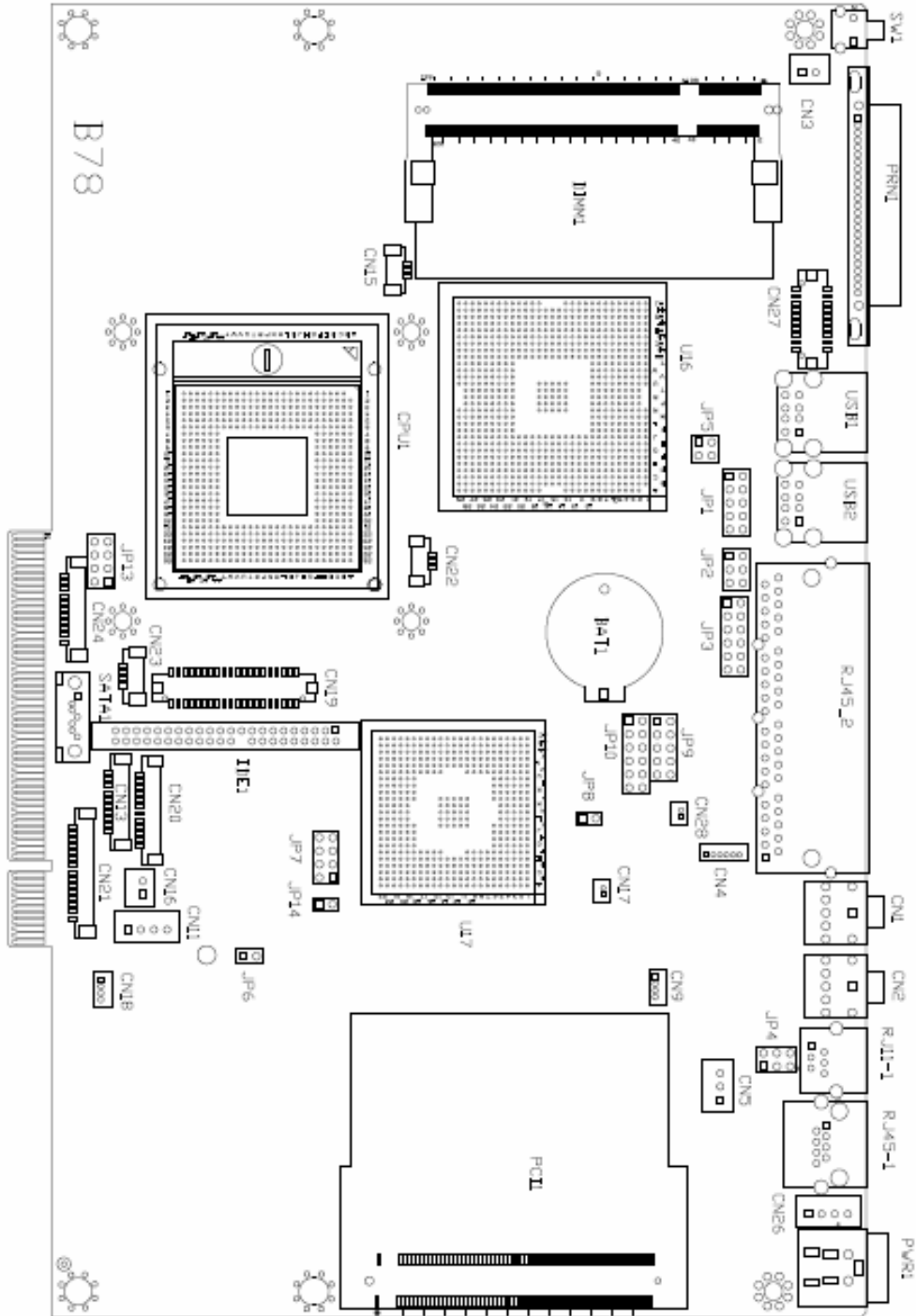


6. Jumper Settings

6.1 B78 Jumper Settings

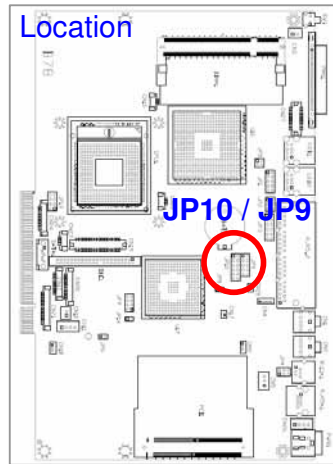
6.1.1 B78 V2.2 Motherboard layout



6.1.2 B78 V2.2 connectors

Connector	Function
BAT1	CMOS Battery Base (Use CR2023)
CN1	Audio Line Out
CN2	Audio MIC In
CN3	Internal Power Switch
CN4	Speaker & MIC Connector
CN9	CD-IN Connector
CN11	Power Connector For 3.5" HDD
CN13	COM5 for Touch
CN15	CPU FAN Connector
CN16	Hardware Reset
CN18	USB2
CN19	LCD Interface Connector
CN20	Inverter Connector
CN21	Card Reader Connector
CN22	System FAN Connector
CN23	IrDA Connector
CN24	FT Status Interface
CN26	Internal Power In Connector
CN27	Internal LPT Connector
CN28	Internal PCI Reset Output Connector
IED1	Secondary IDE Connector (Pitch = 2.0mm)
PRN1	Parallel Port
PWR1	+19V Power Adaptor
RJ11_1	Cash Drawer Connector
RJ45_1	LAN (On Board)
RJ45_2	COM1, COM2, COM3, COM4
SATA1	SATA Connector
USB1	USB3, USB4
USB2	USB5, USB6
JP1	VGA Port
JP2	VGA Power

6.1.3 B78 V2.2 Jumper settings

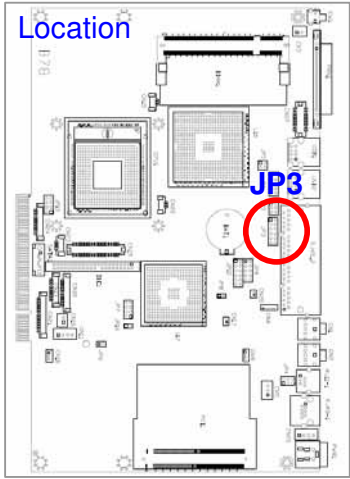


B78 V2.2 COM2 RS232/485/422 Setting

Function	JP10 (1-2) (3-4) (5-6) (7-8) (9-10) (11-12)	JP9 (1-2) (3-4) (4-6) (5-7) (7-8) (9-10)																																	
⊙ RS232	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td><td>9</td><td>11</td></tr> <tr><td>■</td><td>□</td><td>□</td><td>□</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td><td>10</td><td>12</td></tr> </table>	1	3	5	7	9	11	■	□	□	□	□	□	2	4	6	8	10	12	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td><td>9</td></tr> <tr><td>■</td><td>■</td><td>■</td><td>■</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td><td>10</td></tr> </table>	1	3	5	7	9	■	■	■	■	□	2	4	6	8	10
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⊙ = Default

B78 V2.2 COM3 & COM4 Power Setting

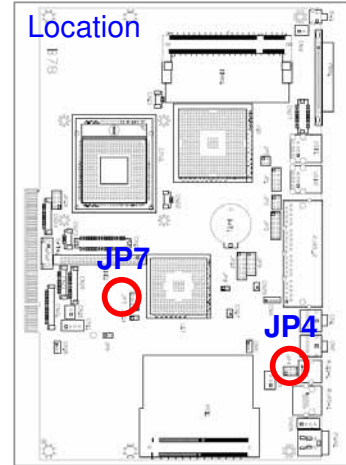
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+5V	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td><td>9</td><td>11</td></tr> <tr><td>□</td><td>■</td><td>□</td><td>□</td><td>□</td><td>□</td></tr> <tr><td>□</td><td>■</td><td>□</td><td>□</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td><td>10</td><td>12</td></tr> </table>	1	3	5	7	9	11	□	■	□	□	□	□	□	■	□	□	□	□	2	4	6	8	10	12		
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COM4 Pin10	⊙RI	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td><td>9</td><td>11</td></tr> <tr><td>□</td><td>□</td><td>□</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>□</td><td>□</td><td>□</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td><td>10</td><td>12</td></tr> </table>	1	3	5	7	9	11	□	□	□	■	□	□	□	□	□	■	□	□	2	4	6	8	10	12	
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+5V	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td><td>9</td><td>11</td></tr> <tr><td>□</td><td>□</td><td>□</td><td>□</td><td>■</td><td>□</td></tr> <tr><td>□</td><td>□</td><td>□</td><td>□</td><td>■</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td><td>10</td><td>12</td></tr> </table>	1	3	5	7	9	11	□	□	□	□	■	□	□	□	□	□	■	□	2	4	6	8	10	12		
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⊙ = Default

B78 V2.2 Cash Drawer Power Setting

Function	JP4 (1-2) (3-4) 5-6												
+12V	<table border="1"> <tr><td>1</td><td>3</td><td>5</td></tr> <tr><td>■</td><td>□</td><td>□</td></tr> <tr><td>■</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td></tr> </table>	1	3	5	■	□	□	■	□	□	2	4	6
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⊙+24V	<table border="1"> <tr><td>1</td><td>3</td><td>5</td></tr> <tr><td>□</td><td>□</td><td>■</td></tr> <tr><td>□</td><td>□</td><td>■</td></tr> <tr><td>2</td><td>4</td><td>6</td></tr> </table>	1	3	5	□	□	■	□	□	■	2	4	6
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□	□	■											
□	□	■											
2	4	6											

⊙ = Default



B78 V2.2 LCD ID Setting

Panel #	Resolution	LVDS		JP7 (1-2) (3-4) (5-6) (7-8)																
		Bits	Channel																	
1	640 x 480	18	Single	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>■</td><td>■</td><td>■</td></tr> <tr><td>■</td><td>■</td><td>■</td><td>■</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	■	■	■	■	■	■	■	2	4	6	8
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■	■	■	■																	
2	4	6	8																	
2	800 x 600	18	Single	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	■	□	□	■	■	□	□	2	4	6	8
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■	■	□	□																	
■	■	□	□																	
2	4	6	8																	
3	1024 x 768	18	Single	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>■</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>■</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	■	□	■	■	■	□	■	2	4	6	8
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■	■	□	■																	
2	4	6	8																	
4	1280 x 1024	24	Dual	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>■</td><td>■</td><td>□</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	■	□	□	■	■	□	□	2	4	6	8
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5	1024 x 768	24	Single	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>□</td><td>■</td><td>■</td></tr> <tr><td>■</td><td>□</td><td>■</td><td>■</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	□	■	■	■	□	■	■	2	4	6	8
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6	800 x 600	24	Single	<table border="1"> <tr><td>1</td><td>3</td><td>5</td><td>7</td></tr> <tr><td>■</td><td>□</td><td>■</td><td>□</td></tr> <tr><td>■</td><td>□</td><td>■</td><td>□</td></tr> <tr><td>2</td><td>4</td><td>6</td><td>8</td></tr> </table>	1	3	5	7	■	□	■	□	■	□	■	□	2	4	6	8
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■	□	■	□																	
2	4	6	8																	

Panel #	Resolution			LVDS		JP7
				Bits	Channel	(1-2) (3-4) (5-6) (7-8)
7	800	x	600	18	Single	
8	800	x	600	18	Single	
9	1024	x	768	24	Single	
10	1440	x	900	24	Dual	
11	1280	x	1024	24	Dual	
12	1440	x	900	18	Dual	